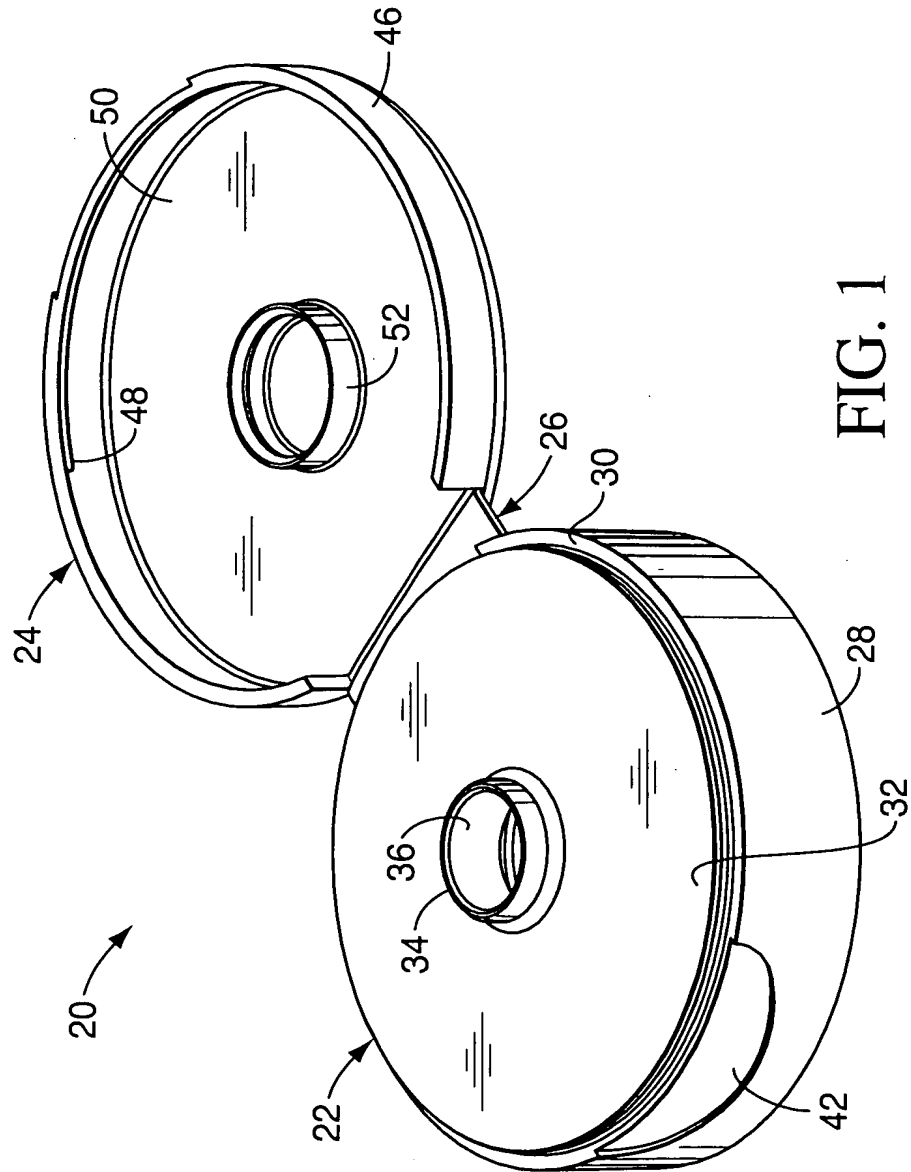


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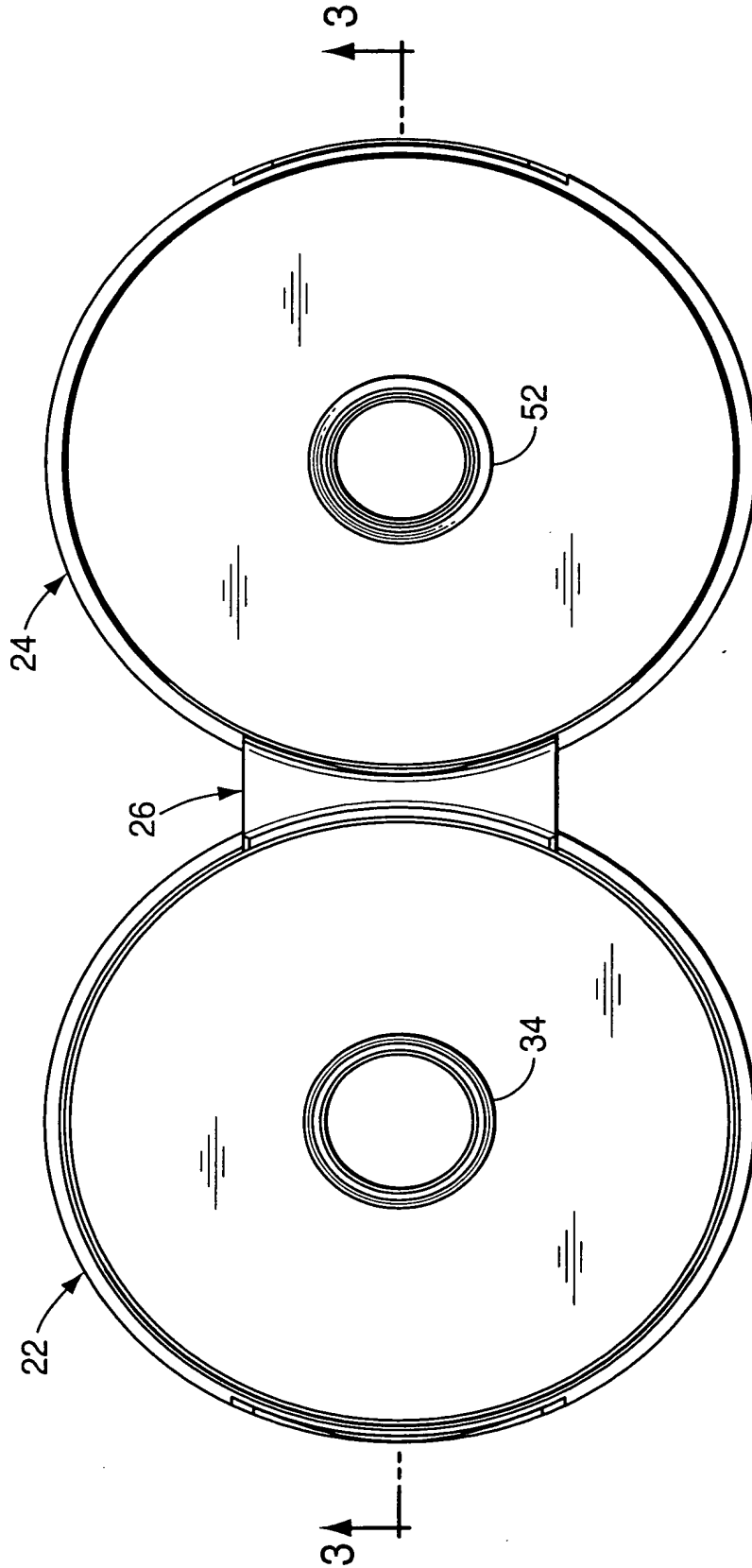


FIG. 2



FIG. 3

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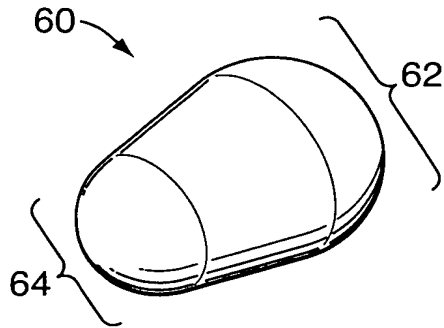


FIG. 4

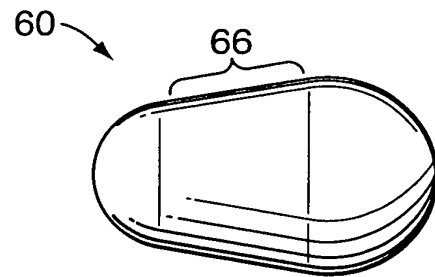


FIG. 5

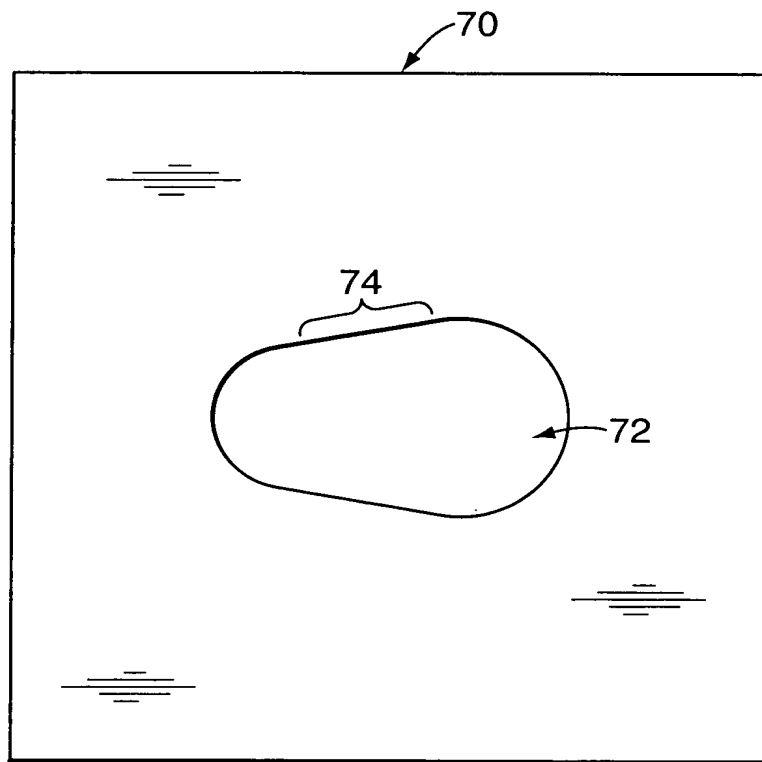


FIG. 6

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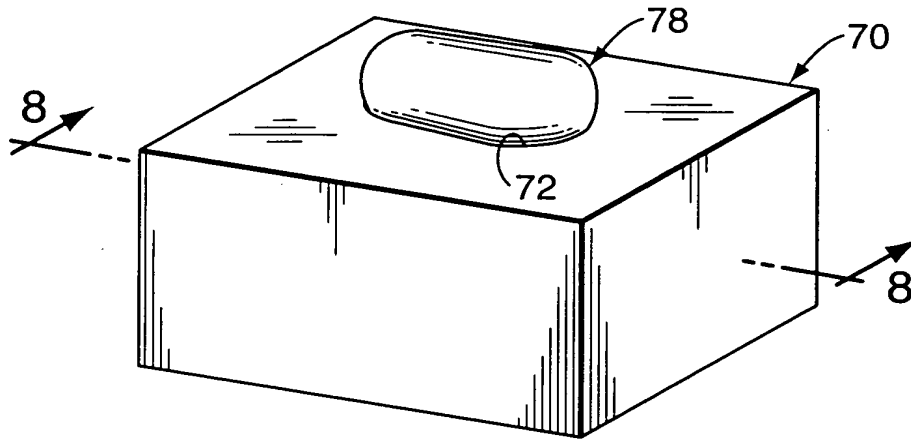


FIG. 7

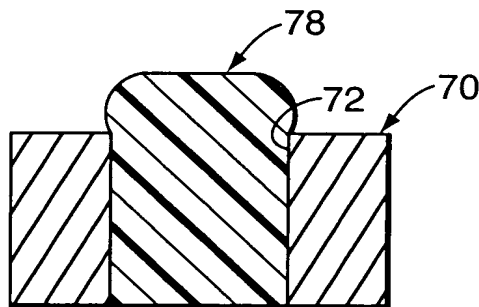


FIG. 8

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FIG. 9

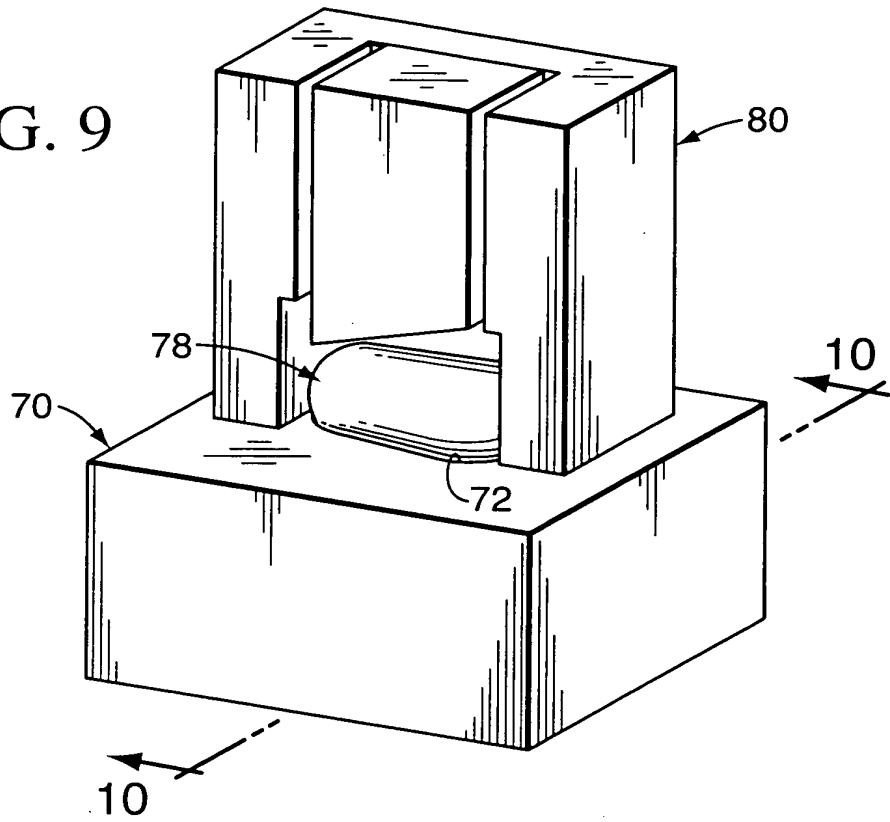
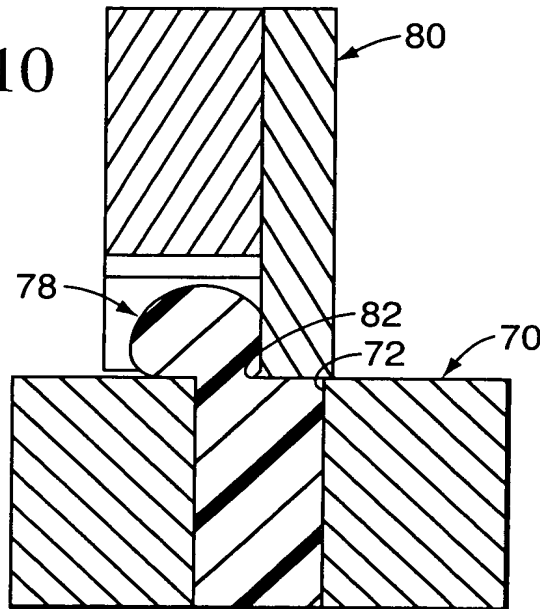


FIG. 10



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FIG. 11

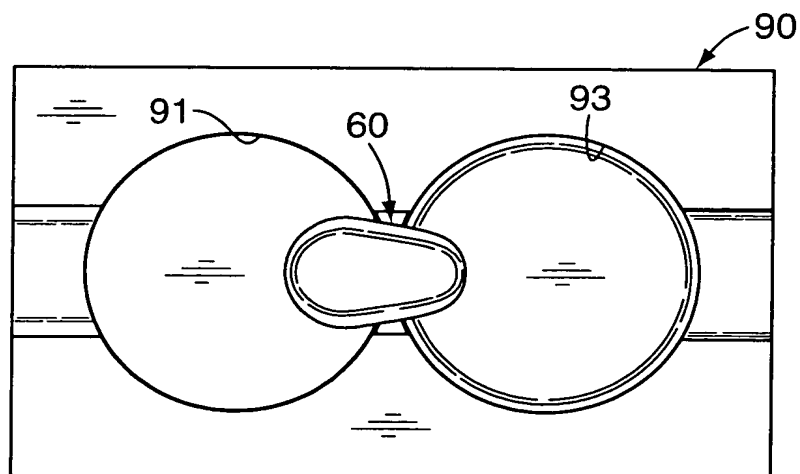
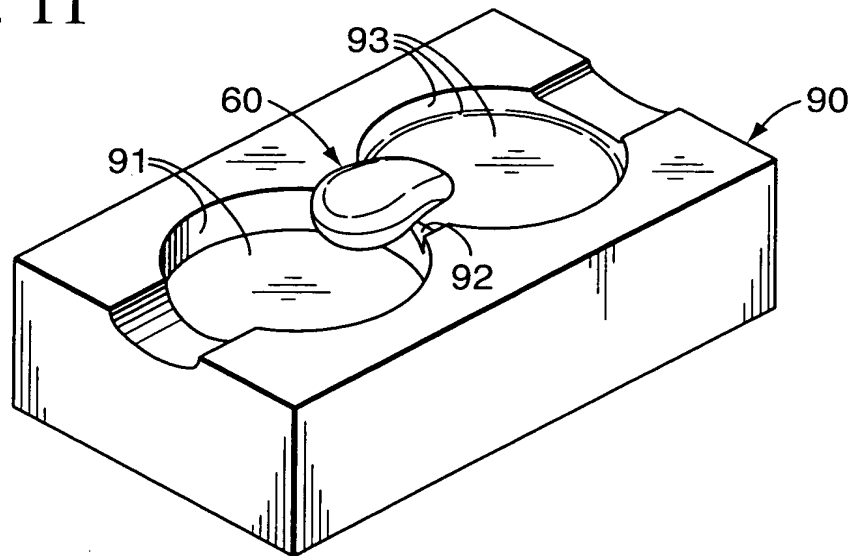


FIG. 12

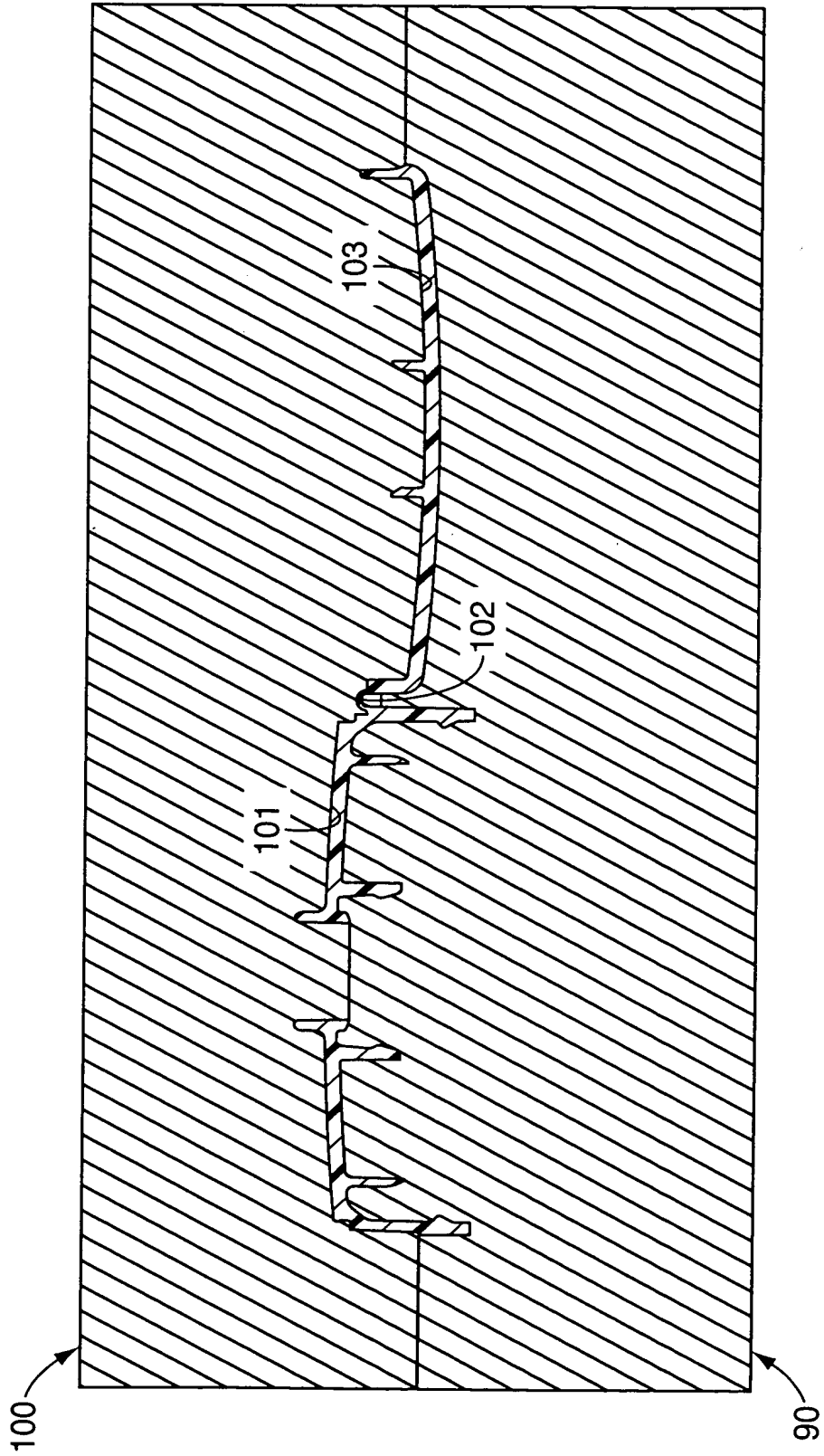


FIG. 13



FIG. 14

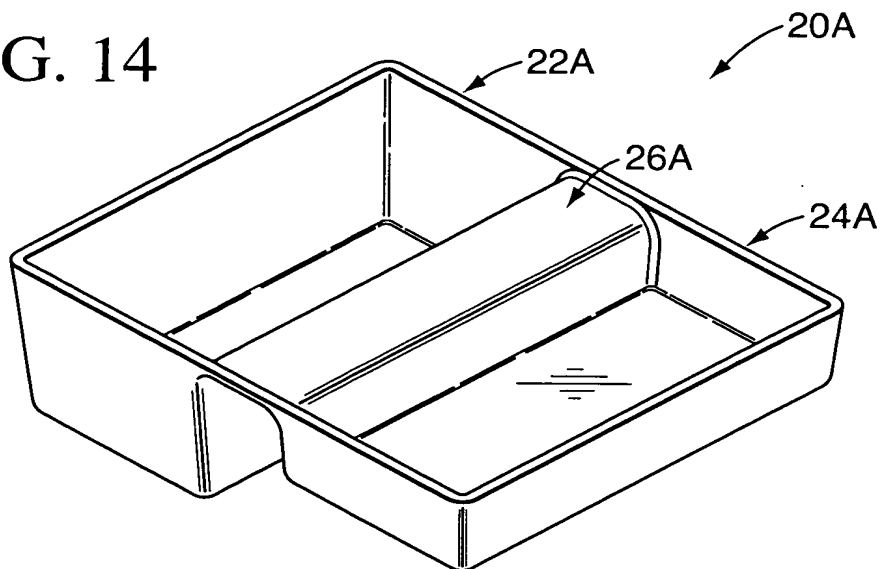
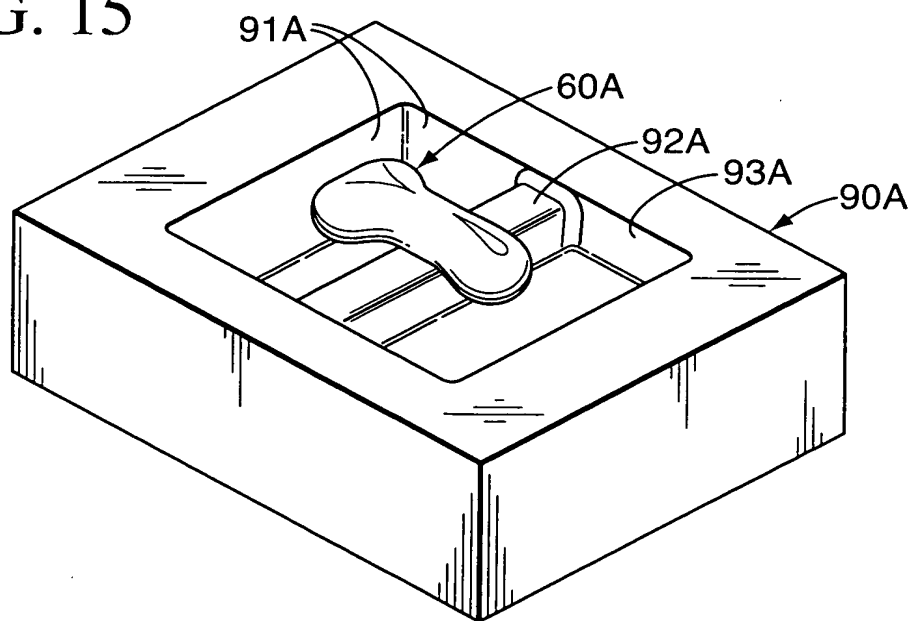


FIG. 15



This diagram shows a cross-sectional view of a second embodiment of the device. It features a substrate 90A with a central opening 91A. A second electrode 60A is positioned on the top surface of the substrate, with its ends extending into the opening 91A. The electrode 60A is connected to a second terminal 62A on the left and a second terminal 67A on the right. A second insulating layer 92A is located on the top surface of the substrate, covering the electrode 60A and the opening 91A. A second gap 93A is formed in the insulating layer 92A, separating the electrode 60A from the substrate 90A.